

REMARKS

Claims 1, 5, 9-17 and 20-22 are pending in the application. Claims 1, 13, and 20 are independent. By the foregoing Amendment, claims 1, 13 and 20 have been amended and claims 3, 7-8 and 19 have been canceled. These changes are believed to introduce no new matter and their entry is respectfully requested.

Objection to Claim 1

In the Office Action, the Examiner objected to claim 1 citing informalities. By the foregoing Amendment, Applicants have amended claim 1 to accommodate the Examiner. Accordingly, Applicants respectfully request that the Examiner reconsider and remove the objection to claim 1.

Rejection of Claims 1, 3, 5, 7-14, 16-17, and 19-22 Under 35 U.S.C. §102(e)

In the Office Action, the Examiner rejected claims 1, 3, 5, 7-14, 16-17, and 19-22 under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,996,632 to Levy et al. (hereinafter "Levy"). Applicants respectfully traverse the rejection.

A claim is anticipated only if each and every element of the claim is found, either expressly or inherently, in a reference. (MPEP §2131 *citing Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628 (Fed. Cir. 1987)). The identical invention must be shown in as complete detail as is contained in the claim. Id. *citing Richardson v. Suzuki Motor Co.*, 868 F.2d 1226,1236 (Fed. Cir. 1989)).

Representative independent claim 1 as amended recites in pertinent part "impedance matching circuitry to match impedance of the logic to the differential transmission line, the impedance matching circuitry comprising: an operational amplifier; a first resistance; a second resistance; a first capacitance; a second capacitance; and a transistor, wherein a first terminal of the first resistance, a first terminal of the second resistance, and a first terminal of the first capacitance are coupled to a minus input of the operational amplifier, wherein a second terminal of the first capacitance is coupled to ground, wherein a plus input of the operational amplifier is coupled to Vcc/2, wherein a second terminal of the first resistance, a second terminal of the

second capacitance, and a first terminal of the second resistance are coupled to a first terminal of the transistor, wherein a second terminal of the second transistor is coupled to Vcc, wherein an output terminal of the operational amplifier is coupled to a second terminal of the transistor, and wherein a third terminal of the transistor is coupled to ground.” Support for these changes according to embodiments of the present invention can be found in Applicants’ Specification at page 28, line 17 to page 29, line 15, and in Figures 8 and 9.

Applicants respectfully submit that *Levy* fails to disclose each and every element of claims 1, 13, and/or 20 either expressly or inherently. For example, *Levy* fails to disclose the specific arrangement of the impedance matching circuitry recited in claims 1, 13, and/or 20, such as the operational amplifier being coupled to the first and second resistors, first and second capacitors, and transistor in the manner recited. As such, Applicants respectfully submit that *Levy* fails to anticipate claims 1, 13, and/or 20 and that claims 1, 13, and/or 20 are thus patentable over *Levy*.

Claims 3, 7-8, and 19 have been canceled rendering the rejection to them moot. Claims 5 and 9-12 properly depend from claim 1. Accordingly, Applicant respectfully submits that claims 5 and 9-12 are patentable for at least the same reasons that claim 1 is patentable. (MPEP §2143.03 (citing *In re Fine*, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988)). Claims 14 and 16-17 properly depend from claim 13. Accordingly, Applicant respectfully submits that claims 14 and 16-17 are patentable for at least the same reasons that claim 13 is patentable. Claims 21-22 properly depend from claim 20. Accordingly, Applicant respectfully submits that claims 5 and 21-22 are patentable for at least the same reasons that claim 20 is patentable. Accordingly, Applicant respectfully requests that the Examiner reconsider and remove the rejection to claims 1, 3, 5, 7-14, 16-17, and 19-22.

CONCLUSION

Applicants submit that all grounds for rejection have been properly traversed, accommodated, or rendered moot, and that the application is in condition for allowance. The Examiner is invited to telephone the undersigned representative if the Examiner believes that an interview might be useful for any reason.

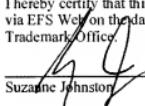
Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: June 1, 2007

/Jan Little-Washington/
Jan Little-Washington
Reg. No.: 41,181
(206) 292-8600

CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being submitted electronically via EFS Web on the date shown below to the United States Patent and Trademark Office.


Suzanne Johnston


6/1/07

Date